

What is claimed is:

1. A receiver capable of receiving a plurality of different codes at a plurality of different frequencies, comprising:

an input device for selection among a plurality of different codes and a plurality of different bit patterns;
an antenna for receiving a receiver actuation signal;
digital frequency control circuitry;
a controller for comparing said received receiver actuation signal to said code and bit pattern selections;
and

output circuitry for responding to the receipt of a receiver actuation signal that matches said code and bit pattern selections.

2. The receiver of claim 1 wherein said digital frequency control circuitry comprises a signal diode capable of adding and removing discrete components from a bandpass filter.

3. The receiver of claim 2 wherein said input device for selecting among a plurality of different codes is a multi-position switch.

4. The receiver of claim 2 wherein said input device for selecting among a plurality of different bit patterns is a DIP switch.

5. A super-regenerative receiver capable of receiving a plurality of different codes at a plurality of different frequencies, comprising: an input device for selection among a plurality of different codes and a plurality of different bit patterns;

an antenna for receiving a receiver actuation signal;
digital frequency control circuitry;

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a controller for comparing said received receiver actuation signal to said code and bit pattern selections; and output circuitry for responding to the receipt of a receiver actuation signal that matches said code and bit pattern selections.

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6. A radio frequency receiver for receiving a plurality of actuation signals from a movable barrier operator transmitter, each receiver being capable of receiving a plurality of coded signals at a plurality of different frequencies, comprising:

first and second user-selectable input devices for selecting a specified code and a specified frequency for receiving said actuation signals;

a controller coupled to said input devices for processing said code and frequency selections and outputting data responsive to said input; and

receiver circuitry responsive to said controller output data for receiving particular actuation signals at one frequency and receiving particular other actuation signals at another frequency.

7. The radio frequency receiver of claim 6, wherein said first user-selectable input device comprises a multi-position switch which determines a particular code to be received as said actuation signal based upon the position of said multi-positioned switch.

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8. The radio frequency receiver of claim 7, wherein said second user-selectable input device comprises a dual in-line packaged switch having a plurality of inner switches which determine a particular bit sequence to be received as said actuation signal based upon the position of said plurality of inner switches.

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9. The radio frequency receiver of claim 8, wherein said controller processes the code and bit sequence selections from said input devices and outputs data according to said input to said receiver circuitry causing said receiver circuitry to receive particular data at one frequency and other data at another frequency.

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10. A method of digitally controlling the frequency of a receiver comprising the steps of:
providing a bandpass filter;
providing a signal diode connecting additional discrete components to said bandpass filter;
providing a controller for controlling the operation of said signal diode to alter the discrete component makeup of the bandpass filter to adjust frequency; and
outputting signals to said diode to alter the bandpass filter frequency.

11. A method of receiving a receiver actuating signal comprising the steps of:
providing a receiver having multiple input devices coupled to a microprocessor and receiver circuitry;
adjusting said receiver circuitry to receive a particular code at a particular frequency based on the position of said multiple input devices and output from said microprocessor; and
receiving said receiver actuating signal.

12. The method of claim 11 wherein one of said multiple input devices is a multi-positioned switch which determines the code to be received as said receiver actuating signal based upon the position of said multi-positioned switch.

13. The method of claim 12 wherein another of said multiple input devices is a dual in-line packaged switch having multiple inner switches which determines a bit pattern to be received as said receiver actuating signal based upon the position of said inner switches.